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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Confirmation No.: 4944

OGAWA et al.

Art Unit: 2816

Serial No.: 10/757,395

Examiner: Kenneth Wells

Filed: January 15, 2004

Docket No.: 108075-00122

For: SEMICONDUCTOR DEVICE HAVING LOGIC CIRCUIT AND MACRO CIRCUIT

**AMENDMENT UNDER 37 C.F.R. §1.111**

**Mail Stop Amendment**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

January 13, 2005

Sir:

This paper is filed in response to the Office Action mailed October 17, 2005, in the above-captioned patent application. In the prosecution of the above-captioned patent application, please consider the following: